


<b>Application Number</b> 	<b>Application/Control No.</b> 10/529,539	<b>Applicant(s)/Patent under Reexamination</b> YOSHIKAWA ET AL.	
<b>Document Code - DISQ</b>	<b>Internal Document – DO NOT MAIL</b>		

<b>TERMINAL DISCLAIMER</b>	<input checked="" type="checkbox"/> <b>APPROVED</b>	<input type="checkbox"/> <b>DISAPPROVED</b>
Date Filed : September 6, 2007	This patent is subject to a Terminal Disclaimer	

<b>Approved/Disapproved by:</b>
Henry D. Jefferson



**TERMINAL DISCLAIMER TO OBTAIN A DOUBLE PATENTING  
REJECTION OVER A "PRIOR" PATENT**

Docket Number (Optional)

NCB-38038

In re Application of: Hiroyuki Yoshikawa

Application No.: 10/529,539

Filed: March 28, 2005

For: WIDE-BAND MODULATION PLL, TIMING ERROR CORRECTION SYSTEM OF WIDE-BAND MODULATION PLL, MODULATION  
TIMING ERROR CORRECTION METHOD AND METHOD FOR ADJUSTING RADIO COMMUNICATION APPARATUS HAVING  
WIDE-BAND

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September 6, 2007  
Date

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